

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Theodore F. Emerson et al.

Confirmation No.:

Application No.: Unassigned

Examiner: Shin, C.

Filing Date: Herewith

Group Art Unit: 2182

Title: METHOD FOR CONTROLLING REMOTE CONSOLE FUNCTIONALITY ASSIST LOGIC

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EV410033962US**

Date of Deposit **11-18-2003**

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: Michael G. Fletcher

Respectfully submitted,

Theodore F. Emerson et al.

By

Michael G. Fletcher

Attorney/Agent for Applicant(s)

Reg. No. **32,777**

Date: **11-18-2003**

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200303930-4		
	APPLICANT		
	Theodor F. Emerson et al.		
	FILING DATE	GROUP	
	H rewith	2182	

REFERENCE DESIGNATION

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	1A	5,257,384	10-26-1993	Farrand et al.	
	1B	5,272,382	12-21-1993	Kochert	
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	1E	5,367,670	11-22-1994	Ward et al.	
	1F	5,402,431	3-28-1995	Saadeh et al.	
	1G	5,410,706	4-25-1995	Farrand et al	
	1H	5,440,699	8-8-1995	Farrand et al	
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Mendel, Brett; "Server I/O All Set to Flow;" Lantimes, October 27, 1997, Vol. 14, Issue 22; cover page and page 31
1R	Briggs, Chris; "Smarter and Faster I/O for Servers;" CORE: Operating Systems; Byte, May 1, 1996, Vol. 2, No. 5
1S	Thompson, Tom; "I2O Beats I/O Bottlenecks;" Byte, August 1997, pp. 85, 86 and 3 additional pages

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	2C	5,898,861	4-27-1999	Emerson et al.	
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	2Q	I2O Introduction; Technology Backgrounder; August 13, 1997; http://www.i2osig.org/Architecture/TechBack.html
	2R	"i960 RP I/O Processor-the I2O SIG site; http://134.134.214.1/design/iio/i2osig.html ; February 6, 1998
	2S	"Welcome to the I2O SIG Web Site!"; http://www.i2osig.org ; February 6, 1998

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	3Q	"About I2O Technology"; http://www.i2osig.org/Architecture ; February 6, 1998
	3R	"Technology Backgrounder"; http://www.i2osig.org/Architecture/TechBack.html ; February 6, 1998; 6 pages
	3S	"Questions and Answers"; http://www.i2osig.org/Architecture/QandA.html ; February 6, 1998; 4 pages

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	4Q	"I2O Specifications For Non-Members"; http://www.i2osig.org/Architecture/GetSpec.html ; February 6, 1998
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5Q	"Press Releases and Clips"; http://www.i2osig.org/Press ; February 6, 1998; 4 pages
5R	Listing of Press Releases; http://altavista.digital.com/cgi-bin/quer.21%2FMar%2F86&d1=&search.x=46&search.y=6 ; February 6, 1998; 2 pages
5S	Crothers, Brooke; "Intel server chip gets big backing"; October 7, 1997; http://www.news.com/News/Item/0,4,14962,00.html ; February 6, 1998

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PATENT APPLICATION

Sheet 6 of 6

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200303930-4	APPLICATION NO.	CONFIRMATION NO.
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	6Q	"HP Demonstrates Commitment to I2O Standard With New I2O Disk-array Controller"; Hewlett Packard; Press Release, Atlanta, Oct. 8, 1997; http://hpcc920.external.hp.com/pressrel/oct97/08oct97b.html ; February 6, 1998; 2 pages
	6R	"I2O: Disaster in the making for the freeware community"; http://22.kenandted.com/i2o/disaster.html ; February 6, 1998; 2 pages
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